

# Kelly M. Brant

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## Engineering Director | Systems & Software

*Lead teams that consistently deliver on their commitments*

Seasoned executive with expertise managing product development from inception through launch.

- Technology Leadership
- Project Management
- Startup Operations
- Systems Engineering
- Budget Management
- Business Planning

## Experience

### **Veredus**, Seaside CA

**Sr. Java Developer**, Application Development

May 2013 – Now

Provide contract java development services to the Department of Defense. Work with product owners to convert business rules into business logic. Refactor legacy code and develop new code on a standard java stack (Oracle 11g, WebLogic, Spring) and in a standard java development environment (eclipse, junit, maven).

### **LearnFind Online**, Redwood City CA

**Consultant**, System Refactoring

Nov 2012 – Apr 2013

Reviewed LearnFind's backend data importer and recommended system updates that use current tools (stable, supported). Installed and configured Hadoop to run Map/Reduce functions that (i) processed large data feeds and (ii) serialized results for further consumption by a key:value store. Refactored language processing to extend service features.

### **Iron Dragonfly**, Palo Alto CA

**Coach**, Professional Performance

2011 – Now

Coach individuals on issues related to job performance and personal effectiveness.

### **Sofia University**, Palo Alto CA

**Graduate Student**, Masters Program - Psychology

2010 – 2012

Began graduate studies part time, moving to full time at the end of SonicSwap's acquisition by Raditaz. My primary area of interest was the individual's impact on group performance and visa-versa.

### **SonicSwap**, Palo Alto CA

**Vice President**, Product Development

2005 – 2011

Managed all java, php software development projects. Analyzed web application systems, synthesized architectures, contributed to and managed engineering of architectures that reduce cost (60%), improved uptime (from crashes per day to crash-free months), and improved web performance (50x.) Managed domestic and offshore (India) engineering. Refactored application: i18n, decoupled XML processing and moved to AWS, decoupled meta-data processing and moved to Flex, and migrated 95% of MySQL data to key:value store. Managed technology transfer and integration during company acquisition (Raditaz.)

**XINC Media Engines**, Palo Alto CA**President**

2001 – 2005

Developed prototype for multi-car synchronized audio playback and light show. Collaborated with a partner company (KeyUp) on R&D for a home jukebox (hard drive).

**PortalPlayer**, Santa Clara CA**Technical Marketing Director**, Embedded Systems

2000 – 2001

Managed three groups: pre-sales engineering support, publications, and application engineering. Created and delivered distributor training (Asia-Pacific) for new embedded system products. Advised customers during the pilot-project manufacturing phase.

**C Level Design**, San Jose CA**Technical Marketing Manager**, Design Methodology

1998 – 2000

Managed product backlog priority. Delivered technical sales training (worldwide) for the company's compiler and its associated C-based hardware modeling methodology.

**Niwa**, Palo Alto CA**Vice President**, Operations

1997 – 1998

Managed engineers (Hong Kong) to complete die procurement, industrial design, micro-controller firmware and PCB for health and fitness personal monitoring devices.

**National Semiconductor**, Santa Clara CA**Technical Marketing Manager**, PC Products

1995 – 1997

Researched industry developments and recommend product strategy for personal computer ICs. Created and delivered sales training for new products (IC).

**Marketing Engineer**, Federal Contracts

1993 – 1995

Managed proposal process for joint public/private R&D projects in the areas of radiation tolerant commercial VLSI processes, smart sensing munitions and information security.

**Applications Engineer**, Graphics Products

1990 – 1993

Designed and fabricated graphics cards (PCB) for new graphics ICs, including marketing collateral: documentation, software, hardware kits, and tutorials. Wrote a compiler (C) to compile device driver routines (x86 assembly) into simulation test vectors (Verilog.)

## Education

**BS Electrical Engineering**

1988

University of Texas at Arlington

**Continuing Studies**

2007 – 2009

Stanford University

(Philosophy; Mandarin 1,2,3)

**Master of Transpersonal Psychology**

2012

Sofia University, Palo Alto

## Certification

**Certified Scrum Master (CSM)**

2011

Scrum Alliance